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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10074732	FILING DATE 02/13/2002	CLASS 257	SUBCLASS 250	GAU 2811	EXAMINER <i>Goldie Lee</i>
**APPLICANTS: Mathew Leo; Nguyen Bich-Yen; Sadd Michael; White Bruce; <i>Broch</i>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		SC11805TP			
Verified and Acknowledged Examiners's initials					
TITLE : Method of forming a vertical double gate semiconductor device and structure thereof					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
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